

(650) 324-6371 Atty: Steven J. Cahill Reg. No. 44,578 Atty. Docket #: 015114-067000US

TECHNIQUES FOR PRELOADING DATA INTO MEMORY ON PROGRAMMABLE CIRCUITS

Inventors: Yan Chong et al. Appln #: 10/646,538

Sheet <u>I</u> of 6

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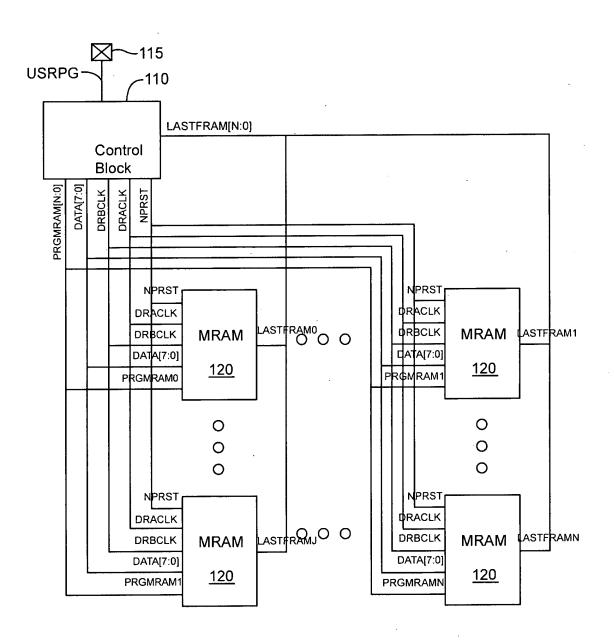


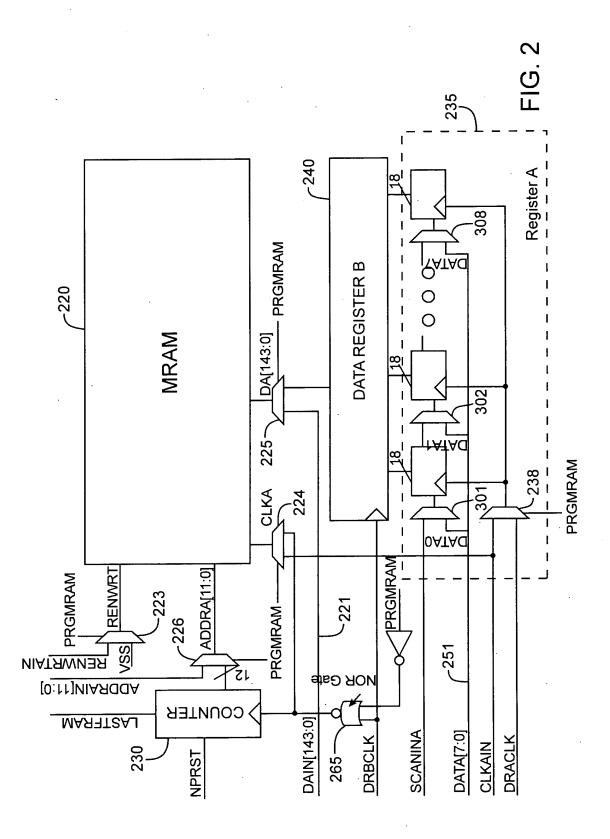
FIG. 1

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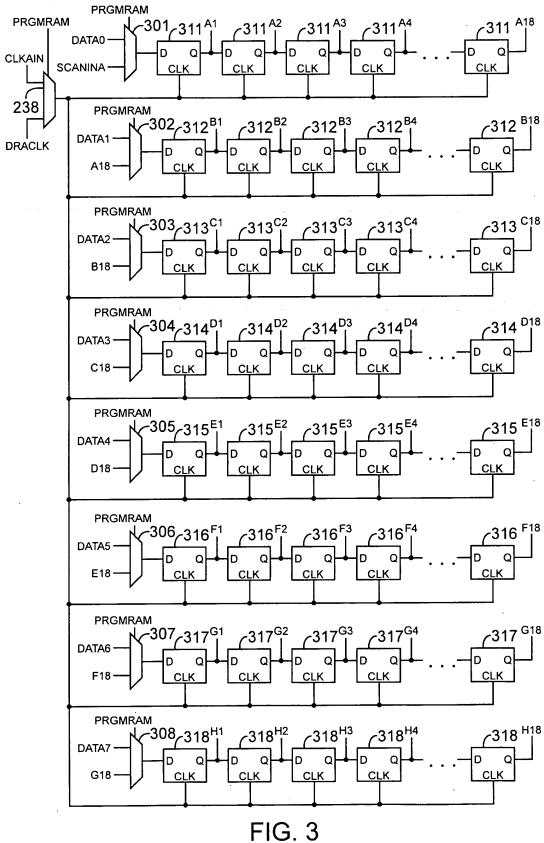
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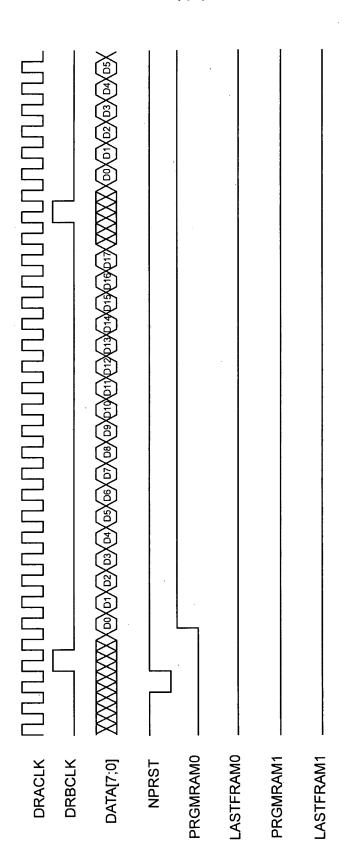
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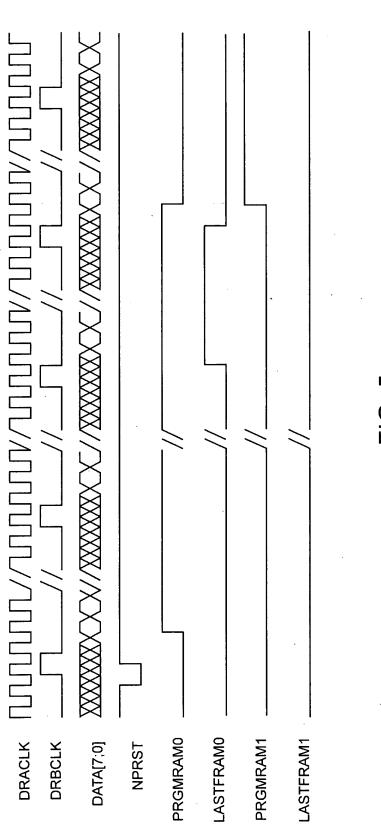
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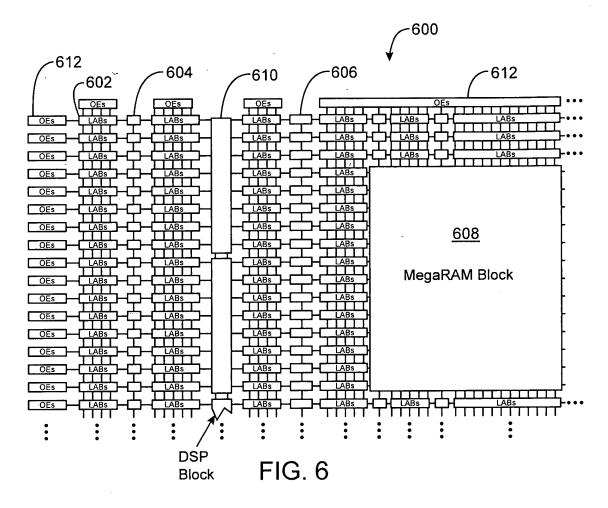


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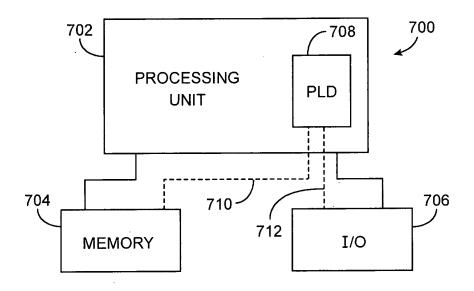


FIG. 7